

ABSTRACT

An HDL circuit conversion and simulation method is described. One or more HDL source modules are converted to simulation program libraries and simulated. The simulation system and method compiles HDL models into linkable libraries. Resulting libraries include calls to the HDL's PLI so that the libraries along with HDL source can be simulated using any simulator of the HDL. The host simulator provides scheduling and system operations that are requested by the linkable simulation program libraries produced by the simulation system here disclosed. The system and method is called an HDL simulator independent PLI based model compiler. The simulation system allows utilization of HDL simulator advances without changing linkable libraries.